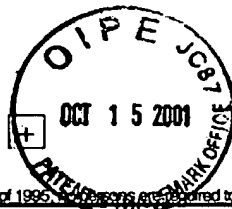


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Sheet 2 of 3

Complete If Known

Application Number	09/747,759
Filing Date	Dec. 21, 2000
First Named Inventor	Kean, Thomas
Group Art Unit	2818
Examiner Name	Unassigned
Attorney Docket Number	019546-020110US

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JA	24	XILINX INC., "Xilinx User Guide and Tutorials", 1991, Section "The Best of Xcell", p602, column headed "Design Security"	<input type="checkbox"/>
JA	25	XILINX INC., "XACT Reference Guide", 1993, Section 5 "Peripherals and Hardware", p602, subsection headed "Programming the XC3720"	<input type="checkbox"/>
JA	26	JOHN G. PROAKIS, "Digital Communications", 3rd edition, McGraw Hill, 1995, Chapter 8, pp413-528	<input type="checkbox"/>
JA	27	JOHN V. OLDFIELD and RICHARD C. DORF, "Field Programmable Gate Arrays", Wiley-Interscience, 1995, Chapter 3, pp53-102	<input type="checkbox"/>
JA	28	BRUCE SCHNEIER, "Applied Cryptography", 2nd Edition. John-Wiley, 1996	<input type="checkbox"/>
JA	29	ACTEL CORP., "Protecting your Intellectual Property from the Pirates", presentation at DesignCon '98, 1998	<input type="checkbox"/>
JA	30	ATMEL CORP., application note "Programming Specification for Atmel's AT17 and AT17A series FPGA configuration EEPROMs", 1999	<input type="checkbox"/>
JA	31	TRISCEND INC., "Triscend E5 Configurable Processor Family", Product Description (Preview), July 1999. Section "System Initialisation" p39-43	<input type="checkbox"/>
JA	32	ACTEL CORP., "60RS Family SPGA's", Advanced Data Sheet, Nov. 19, 1999.	<input type="checkbox"/>
JA	33	XILINX INC., "Virtex 2.5V Field Programmable Gate Arrays," Advanced Product Specification, 1999, pp 1-22	<input type="checkbox"/>
JA	34	TRISCEND INC., "Implementing Secure Remote Updates using Triscend E5 Configurable System-on-Chip Devices", Application Note AN-02, January 2000	<input type="checkbox"/>
JA	35	INTEL CORP., "Intel Processor Identification and the CPUID Instruction", Application Note AP-485, Intel Corporation, Nov. 2000.	<input type="checkbox"/>
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Examiner Signature	<i>JA</i>	Date Considered	7/19/04
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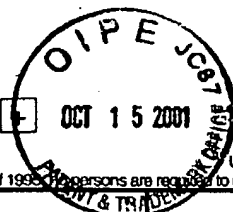
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Application Number	09/747,759
Filing Date	Dec. 21, 2000
First Named Inventor	Kean
Group Art Unit	2818
Examiner Name	Unassigned
Attorney Docket Number	019546-020110US

Sheet 1 of 3

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
JA	01	4,120,030		Johnstone	10-10-1978	
JA	02	4,465,901		Best	08-14-1984	
JA	03	4,525,599		Curran	07-25-1985	
JA	04	4,582,305		Gaffney	12-31-1985	
JA	05	4,603,381		Gutttag	07-29-1986	
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JA	15	5,835,402		Rao	11-10-1998	
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JA	17	5,915,017		Sung	06-22-1999	
JA	18	5,963,104		Buer	10-05-1999	
JA	19	5,970,142		Erickson	10-19-1999	
JA	20	5,991,880		Curd	11-23-1999	

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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
JA	21	WO	98/58305	A1	Deas	12-23-1998		<input type="checkbox"/>
JA	22	WO	00/49717	A2	Trimberger	08-24-2000		<input type="checkbox"/>
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	09/747,759
Filing Date	Dec. 21, 2000
First Named Inventor	Kean, Thomas
Group Art Unit	2818
Examiner Name	Unassigned
Attorney Docket Number	019546-020110US

Sheet	3	of	3
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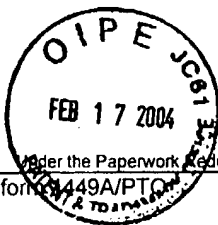
J. Palmer

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STATEMENT BY APPLICANT**

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Sheet

1

of

1

Complete if Known

Application Number	09/747,759
Filing Date	December 21, 2000
First Named Inventor	Thomas A. Kean
Art Unit	2818
Examiner Name	not yet assigned
Attorney Docket Number	13271.5

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	U.S. Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number-Kind Code ² (if known)		
JH	1.	US 6,658,566 B1	12/02/2003	Hazard

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Examiner Initials	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶
		Country ³ Number ⁴ Kind Code ⁵ (if known)			
JH	2.	WO 01/08411 A1	02/01/2001	Credence Systems Corporation	

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